

Program: BE Biomedical Engineering

Curriculum Scheme: Revised 2012

Examination: Third Year Semester V

Course Code: BM502 and Course Name: Microprocessors

Time: 1 hour

Max. Marks: 50

Note: All the Questions are Compulsory and carry equal marks

Q1.	Microprocessor is a part of
Option A:	fan
Option B:	bike
Option C:	computer
Option D:	bicycle
Q2.	Microprocessor understand only
Option A:	English language
Option B:	binary language
Option C:	c language
Option D:	hexadecimal numbers
Q3.	Microprocessor fetches instructions from
Option A:	input devices
Option B:	output devices
Option C:	processing unit
Option D:	memory
Q4.	8086 is how many bit processor
Option A:	4 bit
Option B:	8 bit
Option C:	16 bit
Option D:	32 bits
Q5.	Segment accessed by 8086 is indicated by
Option A:	S3 & S4
Option B:	S0 & S1
Option C:	S5 & S6
Option D:	S2 & S3
Q6.	Segment address, Offset address & Physical address are _____ bits each in 8086
Option A:	8,8 & 16
Option B:	8,16 & 20
Option C:	16,16 & 20
Option D:	8,16 & 20

Q7.	Lower bank & Higher bank are each of maximum_____ in 8086
Option A:	64KB
Option B:	512KB
Option C:	128KB
Option D:	32KB
Q8.	MOV AX,BX instruction is under which addressing mode
Option A:	memory addressing
Option B:	string addressing
Option C:	register addressing
Option D:	implied addressing
Q9.	Immediate operand is ----- data specified in an Instruction
Option A:	constant
Option B:	variable
Option C:	string
Option D:	packet
Q10.	To move constant data 02H in to CL register the instruction used is
Option A:	MOV CL,02H
Option B:	MOV 02H, CL
Option C:	MOV CL, CH
Option D:	MOV CH, CL
Q11.	Effective address is formed using
Option A:	Base register+ Index register+ Displacement
Option B:	Source register+ Index register + displacement
Option C:	Segment register+ Index register + Displacement
Option D:	Base register+ source register+ Displacement
Q12.	MOV[SI],AL belongs to which type of addressing mode
Option A:	base index addressing
Option B:	register direct addressing
Option C:	register indirect addressing
Option D:	register relative addressing
Q13.	Address bus of 8086 is_____ bits
Option A:	8
Option B:	16
Option C:	20
Option D:	32
Q14.	8086 is capable of addressing_____ of memory
Option A:	32 KB
Option B:	64 KB

Option C:	512 KB
Option D:	1 MB
Q15.	How many dedicated interrupts are available on 8086
Option A:	2
Option B:	5
Option C:	8
Option D:	256
Q16.	How many hardware interrupts are available on 8086
Option A:	1
Option B:	2
Option C:	6
Option D:	256
Q17.	How many bytes entry for every ISR in IVT of 8086
Option A:	4
Option B:	3
Option C:	6
Option D:	8
Q18.	Master and Slave PICs are distinguished through software with which ICW in 8259
Option A:	ICW1
Option B:	ICW2
Option C:	ICW3
Option D:	ICW4
Q19.	What will be 8-bit command to mask IR6, IR7 and unmask all other interrupts of 8259
Option A:	C0H
Option B:	D0H
Option C:	B0H
Option D:	A0H
Q20.	How many stack register does an 8087 have?
Option A:	4
Option B:	8
Option C:	16
Option D:	32
Q21.	Which of the following processors can perform exponential, logarithmic and trigonometric functions?
Option A:	8088
Option B:	8086
Option C:	8089

Option D:	8087
Q22.	How many bits of biased exponent in long real format supported by 8087
Option A:	11
Option B:	8
Option C:	15
Option D:	16
Q23.	How many exception flags in the status register of 8087
Option A:	5
Option B:	6
Option C:	7
Option D:	8
Q24.	The short real format supported by 8087 is of
Option A:	8 bits
Option B:	16 bits
Option C:	32 bits
Option D:	64 bits
Q25.	If programmed for round towards negative infinity, the number - 3.44 will be rounded to
Option A:	-4
Option B:	-3
Option C:	0.44
Option D:	-3.44