


THADOMAL SHAHANI ENGINEERING COLLEGE

EXAMINATION TIME TABLE (SEPTEMBER/OCTOBER 2020)
PROGRAMME - T.E. (Electronics & Telecommunication)(REV. -2016) (Choice Based)
SEMESTER - V

Days and Dates	Time	Course Code	Paper
Friday, 25 September 2020	11.30 a.m to 12.30 p.m	ECC501	Micropocessor & Peripherals Interfacing
Sunday, 27 September 2020	11.30 a.m to 12.30 p.m	ECC502	Digital Communication
Tuesday, 29 September 2020	11.30 a.m to 12.30 p.m	ECC503	Electromagnetic Engineering
Tuesday, 29 September 2020	4.30 p.m to 5.30 p.m	ECC504	Discrete Time Signal Processing
Thursday, 1 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5011	Elective I : Microelectronics
Thursday, 1 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5012	Elective I : TV & Video Engineering
Thursday, 1 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5013	Elective I : Finite Automata Theory
Thursday, 1 October 2020	11.30 a.m to 12.30 p.m	ECCDLO 5014	Elective I : Data Compression & Encryption

Change if any, in the time table shall be communicated on the college web site.

Mumbai - 50.
19th September, 2020.


Dr. G. T. Thampi
Principal
PRINCIPAL
THADOMAL SHAHANI ENGINEERING COLLEGE
ADV. NARI GURSAHANI MARG, 37TH ROAD,
(OFF LINKING ROAD), TPS III,
BANDRA (WEST), MUMBAI - 400 050

