THADOMAL SHAHANI ENGINEERING COLLEGE

Days and Dates	Time	Course Code	Paper
Thursday, December 03, 2020	4.30 p.m. to 5.30 p.m.	ETC601	DIGITAL COMMUNICATION
Saturday, December 05, 2020	4.30 p.m. to 5.30 p.m.	ETC602	DISCRETE TIME SIGNAL PROCESSING
Tuesday, December 08, 2020	4.30 p.m. to 5.30 p.m.	ETC603	COMPUTER COMMUNICATION AND TELECOM NETWORKS
Thursday, December 10, 2020	4.30 p.m. to 5.30 p.m.	ETC604	TELEVISION ENGINEERING
Saturday, December 12 , 2020	4.30 p.m. to 5.30 p.m.	ETC605	OPERATING SYSTEMS
Tuesday, December 15, 2020	4.30 p.m. to 5.30 p.m.	ETC606	VLSI DESIGN

EXAMINATION TIME TABLE (DECEMBER 2020)

T.E.(ELECTRONICS & TELE-COMMN.)(Sem VI) (REV-2012) (CBSGS)

Change if any, in the time table shall be communicated on the college web site.

Mumbai 10th November 2020

Dr. G. T. Thampi Principal



PRINCIPAL THADOMAL SHAHANI ENGINEERING COLLEGE ADV. NARI GURSAHANI MARG. 37TH ROAD, (OFF LINKING ROAD), TPS III, BANDRA (WEST), MUMBAI - 400 050