Program: Computer Engineering
Curriculum Scheme: Rev2016/2012
Examination: Second Year Semester-III
Course Code: __and
Course Name: E.C.C.F

Time: 1 hour   Max. Marks: 50

For the students:- All the Questions are compulsory and carry equal marks
(In sample paper on;y 10 ques of 2 marks each)

Q1.	Oscillators is an electronic circuit to generate
Option A:	**Periodic wave**
Option B:	Non periodic wave
Option C:	DC signal
Option D:	None of the above
Q2.	The power amplifier are classified as Class A and Class C based on
Option A:	Transmission capability
Option B:	Power efficiency
Option C:	Conduction angle of BJT used in it
Option D:	Number of BJT used in the circuit
Q3.	Slope overload error occurs in _____-
Option A:	PAM
Option B:	PWM
Option C:	Delta Modulation
Option D:	Adaptive Delta Modulation
Q4.	Voltage divider biasing can be used to give _____ Q point in dc characteristics
Option A:	Stable

Option B:	Unstable
Option C:	Variable
Option D:	On x axis
Q5.	CE amplifier using BJT gives _____ amplification
Option A:	Current
Option B:	Voltage
Option C:	Both A and B
Option D:	DC
Q6.	CMRR in op – amp is an abbreviation for
Option A:	Common mode rejection ratio
Option B:	Common Mode reversal radio
Option C:	Common mode receive of radio
Option D:	Calculative mode rejection ratio
Q7.	The information content in the message is _____proportional to the probability of occurrences of the message
Option A:	Directly
Option B:	Inversely
Option C:	Logarithmic
Option D:	Log to base2
Q8.	The amplitude of the pulse _____in pulse width modulation
Option A:	Increases
Option B:	Decreases
Option C:	Constant
Option D:	Exponential

Q9.	**What is the carrier frequency in an AM wave when its highest frequency component is 850Hz and the bandwidth of the signal is 50Hz?**
Option A:	80Hz
Option B:	695Hz
Option C:	625Hz
Option D:	825Hz
Q10.	**In Superhetrodyne FM receiver the RF mixer is used to**
Option A:	Addition of two signals
Option B:	Multiplication of two signals
Option C:	Rejection of noise
Option D:	Rejection of carrier