

THADOMAL SHAHANI ENGINEERING COLLEGE

TE

\*\*\*\*\* TIME TABLE \*\*\*\*\*

TE/V/EXTC

Professor :

W.E.F. : 11<sup>th</sup> July 2022

Day/Time	8:00-9:00	9:00-10:00		10:00-11:00	11:15-12:15		1.00-2.00	2.00-3.00	3:00-4:00	4:00-5.00
Monday	DTSP (DT)	DTSP (DT)	T	DVLSI (UB)	RSA (AR)	L	A2 PCE II A1 MPROJ 2A (DP)602	(CB) 605 OB		
Tuesday	DTSP (DT)	DSA (MSK)	E A	A2 DC A1 DTSP A3 PCE II	(AR)706 (DT)609 (CB) 605 OB	U N C	A3 MPROJ 2A(DP)602 A1/A2 RSA (MR)	A2 MPROJ 2A	A3 RSA (DP)602	(MR)
Wednesday	DSA (MSK)	DSA (MSK)	B R	DC (AR)	DVLSI (UB)	H B	A2 MPROJ 2A(JK) A3 DVLSI A1 PCE II	(UB)602 (CB)605 OB	A3 MPROJ 2A(UB)701	
Thursday		RSA (MR)	E A	A2 MWE (MR)	DC (AR)	R E	A3 DTSP A1 DC A2 DVLSI	(DT)609 (AR)706 (UB)602	A1 MPROJ 2A(AH)701	
Friday	PCE-II (CB)	PCE-II (CB)	K	A2 DTSP A1 DVLSI A3 DC	(DT)609 (UB)602 (AR)	A K	DC (AR)	DVLSI (UB)		

Subjects

DC-Digital Communication

DVLSI-Digital VLSI

RSA- Random Signal Analysis

DTSP-Discrete Time Signal Processing

PCE-Professional Communication and Ethics

Data-Data Structures and Algorithm

Faculty

AR-Anuradha Rao

UB-Uttara Bhatt

MR-Maniroja

DT-Dhananjay T

MK- Manoj Kavedia

CB- Dr Chumki Biswas

Faculty

AH-Amit Hatekar

JK- Jyoti Kashyab

DP-Deepak Pawar

HOD EXTC

PRINCIPAL

