

THADOMAL SHAHANI ENGINEERING COLLEGE

TE 806

***** TIME TABLE *****

TE/V/EXTC

Professor :

W.E.F. : 10th July 2023

Day/Time	8:00-9:00	9:00-10:00	10:15-11:15	11:15-12:15		1.00-2.00	2.00-3.00	3:00-4:00	4:00-5.00
Monday		RSA (BI)	DSA (MSK)	DC (AR)	A4 MPROJ(MR) 701 L A2 DTSP (DT) 602 A1 DC (AR) 706 U A3 MPROJ (UB) 701				
Tuesday	A3 DTSP	(DT)602 A1/A2 RSA (MR/BI) 806	E	PCE II (BP)	PCE II (BP)	N	DVLSI (UB)	DVLSI (UB)	DSA (MSK)
Wednesday	A1 DTSP	(DT)602 A3/A4 RSA (MR/BI) 806	B	RSA (BI)	DVLSI (UB)	H	A4 DVLSI (UB)609 A2 DC (AR)706 A1 MPROJ 2A(BI) 701	A2 DVLSI A4 DC (AR)	(UB) 609 706
Thursday	DTSP (DT)	DTSP (DT)	E	A1 PCE II A2 PCE II A3 DC A4 DTSP	(BP) (BP) (AR)706 (DT) 602	R	DC MPROJ2A (AR) (MSK)	MPROJ2A (MSK)	
Friday	DTSP (DT)	RSA (MR)	K	DSA (MSK)	DC (AR)	A	A4 PCE II(BP) A2 MPROJ 2A(MSK)701 A3 PCE II(BP) A1 DVLSI (UB) 609	A3 DVLSI (UB) 609	

Subjects

Faculty

DC-Digital Communication

AR-Anuradha Rao

DVLSI-Digital VLSI

UB-Uttara Bhatt

RSA- Random Signal Analysis

MR-Dr. Maniroja

DTSP-Discrete Time Signal Processing

DT-Dhananjay T

PCE-Professional Communication and Ethics

MSK- Manoj Kavedia

Data-Data Structures and Algorithm

BI-Bharti Ingale

HOD EXTC

PRINCIPAL

