

THADOMAL SHAHANI ENGINEERING COLLEGE

TE

806

***** TIME TABLE *****

TE/V/EXTC

W.E.F. : 7th July 2025

Day/Time	8:00-9:00	9:00-10:00	10:15-11:15	11:15-12:15	1.00-2.00	2.00-3.00	3:00-4:00	4:00-5.00
Monday	DTSP (DT)	DTSP (DT)	DC (AR)	DVLSI (UB)	A1 DVLSI A2 PCE A3 DC	(UB) (602) (BP)(803) (AR) (706)		
Tuesday	RSA (BI)	DCE(MR)806 DSA(MSK)609	DCE (MR)806 DSA(MSK)609	DC (AR)	A4 PCE A1 DTSP A2 DC A3 DVLSI	(BP) (803) (DT) (602) (AR) (706) (UB)		
Wednesday	A1/A3 RSA (BI)	RSA (BI)	PCE (BP)	DC (AR)	A4 DTSP A1 DC A3 MPR 2A (MSK) (701) A2 DVLSI (UB) 602	(DT) (602) (AR) (706) A4 DVLSI (UB) (602) A2 MPR 2A(MSK)(701)		
Thursday	A4/A2 RSA (BI)	RSA (BI)	A1 MPR 2A (TD) (701) A2 DTSP (DT) (602) A3 PCE (BP) (803) A4 DC (AR)(706)		DCE/DSA (MR/MSK) (806/609)	MPROJ 2A (MSK)	MPROJ 2A (MSK)	
Friday	A3 DTSP	(DT) (602)	DTSP (DT)	DVLSI (BP)	DVLSI (UB)	PCE DVLSI (BP)	A1	PCE 806

Subjects

DC-Digital Communication
DVLSI-Digital VLSI
RSA- Random Signal Analysis
DTSP-Discrete Time Signal Processing
PCE-Professional Communication and Ethics
Data-Data Structures and Algorithm
DCE- Data Compression and Encryption

Faculty

AR-Prof.Anuradha Rao
UB-Prof.Uttara Bhatt
BI-Prof.Bharti Ingale
DT-Prof.Dhananjay T
DR Bhaswati Parasar
MSK- Dr. Manoj Kavedia
MR. Dr. Maniroja

HOD EXTC

PRINCIPAL

