



THADOMAL SHAHANI
TSEC
ENGINEERING COLLEGE

ELECTROVERSE.COMM TSEC PRESENTS

TECH-A-THON 2026

HARDWARE HACKATHON

POWERED BY



THADOMAL SHAHANI ENGINEERING
COLLEGE

ELECTRONICS AND
TELECOMMUNICATIONS DEPARTMENT



ABOUT THE COLLEGE

THADOMAL SHAHANI ENGINEERING COLLEGE (TSEC) IS A PRIVATE ENGINEERING COLLEGE IN MUMBAI, INDIA. FOUNDED IN 1983, IT IS THE FIRST AND OLDEST PRIVATE ENGINEERING INSTITUTE AFFILIATED WITH THE UNIVERSITY OF MUMBAI. TSEC WAS FOUNDED BY THE HYDERABAD (SIND) NATIONAL COLLEGIATE BOARD (HSNC BOARD) IN 1983. IT IS NAMED AFTER ONE OF MUMBAI'S MOST RESPECTED PHILANTHROPISTS, DADA KISHINCHAND T. SHAHANI'S FATHER, THADOMAL SHAHANI.

APPROVED BY THE DIRECTORATE OF TECHNICAL EDUCATION OF MAHARASHTRA ON 16 SEPTEMBER 1983, TSEC IS ONE OF THE OLDEST PRIVATE ENGINEERING COLLEGES IN INDIA AND WAS AMONG THE FIRST INSTITUTES IN THE COUNTRY TO OFFER UNDERGRADUATE-LEVEL STUDIES IN SPECIALIZATIONS SUCH AS COMPUTER ENGINEERING, INFORMATION TECHNOLOGY, AND BIOMEDICAL ENGINEERING. THE DEPARTMENT OF BIOMEDICAL ENGINEERING IS ONE OF THE OLDEST IN INDIA AND WAS SET UP IN 1983. THE FIRST BATCH OF COMPUTER ENGINEERING GRADUATES PASSED OUT IN 1988. THE UNDERGRADUATE COURSE IN INFORMATION TECHNOLOGY WAS STARTED IN 1998. THE DEPARTMENTS OF ELECTRONICS & TELECOMMUNICATION ENGINEERING AS WELL AS CHEMICAL ENGINEERING WERE ESTABLISHED IN 1983, WHEREAS THAT OF BIOTECHNOLOGY WAS ESTABLISHED IN 2004. THE UNDERGRADUATE COURSE IN ARTIFICIAL INTELLIGENCE AND DATA SCIENCE WAS STARTED IN 2020.

DR. G.T. THAMPI
PRINCIPAL

(CONVENER)



ABOUT THE DEPARTMENT

DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION WAS ESTABLISHED IN YEAR 2002 WITH A CLEAR VIEW OF A FORERUNNER IN THE FIELD OF TELECOMMUNICATIONS, THE DEPARTMENT SEEKS TO EVOLVE TOWARDS EXCELLENCE AND ADAPT ITSELF TO THE RAPID ADVANCEMENTS IN SPECIALIZED COMMUNICATION RELATED FIELDS. THIS DEPARTMENT EARLIER HAD NOMENCLATURE OF ELECTRONICS ENGINEERING FROM 1983 TO 2001. FROM 2002 STUDENT INTAKE FOR E&TC HAS BEEN 60 STUDENTS. AT THE DESKTOP OF ACHIEVEMENTS OF THE DEPARTMENT, LIES THE AWARD OF ACCREDITATION GIVEN BY THE NATIONAL BOARD OF ACCREDITATION SINCE 2008. THE DEPARTMENT IS ACCREDITED BY NBA CONTINUOUSLY FOR THE PAST 4 CYCLES AND RECENTLY BY NAAC. THE DEPARTMENT HAS ALSO STARTED A PHD PROGRAM, WITH AN INTAKE OF 10 STUDENTS FROM THE ACADEMIC YEAR 2014-15. THE CURRICULUM OF THE PROGRAM HAS BEEN DESIGNED TO CATER TO THE EVER CHANGING NEEDS AND DEMANDS OF COMMUNICATION INDUSTRY. THE FOCUS IS ON HOLISTIC LEARNING AND EMPOWERING STUDENTS TO MAKE SIGNIFICANT CONTRIBUTIONS AT ALL LEVELS. THE OPEN ELECTIVE SYSTEM GIVES A HANDFUL OF CHOICES FOR STUDENTS TO DEVELOP EXPERTISE IN THEIR AREA OF INTEREST SUCH AS NEURAL NETWORKS, RADAR ENGINEERING, VLSI, DATA COMPRESSION AND ENCRYPTION, SATELLITE COMMUNICATION, IMAGE PROCESSING, ARTIFICIAL INTELLIGENCE, AND MACHINE LEARNING ETC. IT HAS ONE OF THE BEST STATE-OF-THE-ART HARD WARE AND SOFTWARE LABORATORIES WITH SPECIAL FOCUS ON TELECOMMUNICATION.

**DR. MANIROJA
EDINBURGH**

**HEAD OF DEPARTMENT
EXTC**

(EVENT CO-CONVENER)

PROF MANOJ KAVEDIA

ASSISTANT PROFESSOR

PROF BHARATI INGALE

ASSISTANT PROFESSOR

(EVENT COORDINATORS)

**ELECTRONICS AND
TELECOMMUNICATIONS DEPARTMENT**



TECH-A-THON 2026

HARDWARE HACKATHON

PRIZE POOL



₹15,000

1ST RUNNER UP



₹10,000

2ND RUNNER UP



₹25,000

WINNER

**THADOMAL SHAHANI ENGINEERING
COLLEGE**

**ELECTRONICS AND
TELECOMMUNICATIONS DEPARTMENT**



TECH-A-THON 2026

HARDWARE HACKATHON

WE, ELECTROVERSE.COMM-TSEC, A TECHNICAL COMMITTEE OF THE EXTC DEPARTMENT OF THADOMAL SHAHANI ENGINEERING COLLEGE, MUMBAI, MAHARASHTRA, INVITE YOU TO TECH-A-THON, A 48-HOUR HARDWARE HACKATHON.

GET READY TO SWIRL IN THE VORTEX OF INNOVATION AND SHOWCASE YOUR POTENTIAL TO OFFER FEASIBLE SOLUTIONS FOR REAL WORLD PROBLEMS WITH A TECHNICAL EDGE.



DATE

26, 27 &
28 FEB.



VENUE

TSEC NEW
BLDG

THADOMAL SHAHANI ENGINEERING
COLLEGE

ELECTRONICS AND
TELECOMMUNICATIONS DEPARTMENT



TIMELINE

ROUND 1 (ONLINE)
FROM 1ST FEB-6TH FEB.
SHORTLISTED TEAMS
QUALIFY FOR ROUND 2

REGISTRATIONS
START FROM
10TH JAN

REGISTRATIONS CLOSES
ON 20TH JAN

ROUND 2 (FINAL
IMPLEMENTATION)
BEGINS ON 26TH
FEB 9:30 AM AND
ENDS ON 28TH FEB
2:00PM

TOP 3 TEAMS WILL
BE DECLARED AS
WINNERS.

TECH-A-THON 2026



ABOUT TECHATHON

TECH-A-THON OFFERS STUDENTS A PLATFORM TO DESIGN AND DEVELOP INNOVATIVE SOLUTIONS FOR REAL-WORLD PROBLEM STATEMENTS, STRENGTHENING THEIR PROBLEM-SOLVING AND TECHNICAL SKILLS. THIS HARDWARE-FOCUSED HACKATHON EMPHASIZES HANDS-ON IDEATION, STRUCTURED EVALUATION, AND RAPID EXECUTION, ENABLING PARTICIPANTS TO TRANSFORM CONCEPTS INTO FUNCTIONAL WORKING MODELS. PARTICIPANTS ARE ENCOURAGED TO PUSH THE BOUNDARIES OF INNOVATION BY EFFECTIVELY UTILIZING THEIR TECHNICAL EXPERTISE TO BUILD PRACTICAL AND IMPACTFUL SOLUTIONS.

ELIGIBILITY: STUDENTS PURSUING B.E. / B.TECH FROM ANY DEGREE COLLEGE OR STUDENTS FROM ANY TECHNICAL DIPLOMA COLLEGE. (OPEN TO ALL BRANCHES)

TEAM SIZE: A MAXIMUM OF 4 MEMBERS PER TEAM

ROUND 1 – IDEA SCREENING (ONLINE)

PARTICIPATING TEAMS ARE REQUIRED TO CONCEPTUALIZE A SOLUTION FOR ONE OF THE PROVIDED DOMAINS. TEAMS MUST PRESENT THEIR IDEA, PROPOSED IMPLEMENTATION APPROACH, AND A BRIEF OVERVIEW OF THE COMPONENTS TO BE USED ALONG WITH THEIR FUNCTIONALITY. THIS ROUND WILL BE CONDUCTED ONLINE, WITH A PRESENTATION DURATION OF 10 MINUTES.

SHORTLISTED TEAMS WILL QUALIFY FOR THE NEXT ROUND.

ROUND 2 – IMPLEMENTATION & DEMONSTRATION

THE FINAL ROUND FOCUSES ON THE DEVELOPMENT AND EXECUTION OF THE PROPOSED SOLUTION. QUALIFIED TEAMS WILL BUILD A WORKING HARDWARE/SOFTWARE PROTOTYPE BASED ON THEIR IDEA.

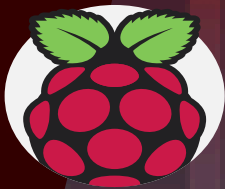
TEAMS WILL BE ALLOTTED 48 HOURS TO COMPLETE THE IMPLEMENTATION. UPON COMPLETION, THE FINAL PROTOTYPE MUST BE DEMONSTRATED AND PRESENTED TO THE JUDGING PANEL.



WORKING MODEL MUST BE DEVELOPED USING AT LEAST ONE OF THE FOLLOWING COMPONENTS

1 ARDUINO

AN OPEN-SOURCE
MICROCONTROLLER PLATFORM
WIDELY USED FOR BUILDING
ELECTRONIC PROJECTS.



2 RASPBERRY PI

A COMPACT COMPUTING DEVICE
COMPATIBLE WITH VARIOUS INPUT AND
OUTPUT PERIPHERALS.

3 STM 32

A MICROCONTROLLER FAMILY
SUPPORTING SERIAL AND PARALLEL
COMMUNICATION WITH DIVERSE
ELECTRONIC COMPONENTS.



4 MSP430

A 16-BIT ULTRA-LOW-POWER
MICROCONTROLLER SUITABLE FOR
SENSING AND MEASUREMENT
APPLICATIONS



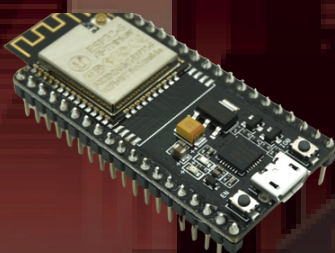
5 FPGA

A PROGRAMMABLE INTEGRATED CIRCUIT
THAT ALLOWS CUSTOMIZATION FOR
APPLICATION-SPECIFIC OPERATIONS.



6 ESP32

A LOW-COST SYSTEM-ON-CHIP (SoC)
MICROCONTROLLER COMMONLY USED FOR
IOT-BASED APPLICATIONS.



NOTE: THE ABOVE LIST IS INDICATIVE. TEAMS ARE PERMITTED TO USE ANY OTHER
SUITABLE DEVELOPMENT BOARD AVAILABLE IN THE COMPONENT INVENTORY.



ADDITIONAL INFORMATION

ROUND 2 WILL BE CONDUCTED IN **OFFLINE** MODE AT THADOMAL SHAHANI ENGINEERING COLLEGE, BANDRA, MUMBAI, MAHARASHTRA FROM **26TH FEBRUARY TILL 28TH OF FEBRUARY, 2026** THE PARTICIPATING TEAMS WILL BE REQUIRED TO STAY ON CAMPUS FOR THE ENTIRE DURATION OF THE HARDWARE HACKATHON. ALL PARTICIPANTS MUST ARRIVE ON CAMPUS BY **9:30 AM** ON FEBRUARY 26TH, 2026.



THE PARTICIPATING TEAMS WILL BE REQUIRED TO COME UP WITH INNOVATIVE SOLUTIONS FOR MODERN- DAY PROBLEMS USING SPECIFIED COMPONENTS. THE COMPONENTS REQUIRED TO BUILD THE SOLUTION ARE TO BE INFORMED BY THE QUALIFIED TEAMS AFTER ROUND 1. PARTICIPANTS WILL HAVE **ACCESS TO THE AVAILABLE COMPONENT INVENTORY**, IF ANY OTHER COMPONENTS ARE REQUIRED, PARTICIPANTS WILL HAVE TO ARRANGE THEM THEMSELVES



COMPONENT INVENTORY WOULD BE PROVIDED IN THE FORM OF A LIST TO ALL PARTICIPANTS. AVAILABILITY CAN BE CONFIRMED BY CONTACTING THE ANY STUDENT COORDINATOR



BREAKFAST, LUNCH, AND DINNER WILL BE PROVIDED . ALL THE TEAMS WILL BE RECEIVING PARTICIPATION CERTIFICATES FROM THADOMAL SHAHANI ENGINEERING COLLEGE AND SOME SPECIAL GOODIES FROM THE ELECTROVERSE.COMM-TSEC TEAM.



DOMAINS

DOMAIN #1

HEALTHCARE AND BIOMEDICAL TECHNOLOGY



ADVANCED HARDWARE SOLUTIONS IN HEALTHCARE AND BIOMEDICAL TECHNOLOGY AIM TO IMPROVE DIAGNOSIS, MONITORING, AND PATIENT CARE. INNOVATIONS SUCH AS MEDICAL DEVICES, HEALTH MONITORING SYSTEMS, AND ASSISTIVE TECHNOLOGIES ENABLE ACCURATE DATA COLLECTION AND EFFICIENT TREATMENT. THESE TECHNOLOGIES PLAY A VITAL ROLE IN ENHANCING HEALTHCARE ACCESSIBILITY AND OUTCOMES.

DOMAIN #2

AGRICULTURAL AND RURAL DEVELOPMENT

HARDWARE-DRIVEN INNOVATIONS IN AGRICULTURE FOCUS ON INCREASING PRODUCTIVITY, SUSTAINABILITY, AND RURAL DEVELOPMENT. SOLUTIONS SUCH AS SMART IRRIGATION, SOIL MONITORING SYSTEMS, AND AUTOMATED FARMING EQUIPMENT HELP OPTIMIZE RESOURCE USAGE AND IMPROVE CROP YIELD. TECHNOLOGY-DRIVEN AGRICULTURE SUPPORTS FOOD SECURITY AND EMPOWERS RURAL COMMUNITIES.





DOMAINS

DOMAIN #3

DEFENCE AND SECURITY SYSTEMS

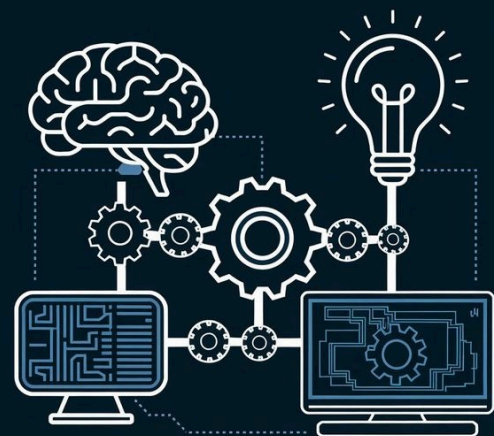
DEFENSE AND SECURITY SYSTEMS LEVERAGE ADVANCED HARDWARE TO ENHANCE SURVEILLANCE, MONITORING, AND THREAT DETECTION. INNOVATIONS INCLUDING SECURE COMMUNICATION SYSTEMS, SENSING DEVICES, AND AUTOMATED DEFENSE MECHANISMS STRENGTHEN NATIONAL AND INFRASTRUCTURAL SECURITY. THESE TECHNOLOGIES ENSURE IMPROVED SITUATIONAL AWARENESS AND RAPID RESPONSE CAPABILITIES.



DOMAIN #4

ROBOTICS AND AUTONOMOUS SYSTEMS

ROBOTICS AND AUTONOMOUS SYSTEMS COMBINE HARDWARE AND INTELLIGENCE TO PERFORM TASKS WITH MINIMAL HUMAN INTERVENTION. APPLICATIONS INCLUDE ROBOTIC ARMS, AUTONOMOUS VEHICLES, AND SMART MACHINES DESIGNED FOR EFFICIENCY AND PRECISION. THESE SYSTEMS PLAY A KEY ROLE IN INDUSTRIES SUCH AS MANUFACTURING, HEALTHCARE, AND EXPLORATION.





DOMAINS

DOMAIN #5

IMMERSIVE AND EDUCATIONAL TECHNOLOGY

IMMERSIVE TECHNOLOGIES ENHANCE LEARNING EXPERIENCES THROUGH INTERACTIVE AND ENGAGING HARDWARE SOLUTIONS. INNOVATIONS SUCH AS VIRTUAL LABS, SIMULATION SYSTEMS, AND SMART EDUCATIONAL TOOLS IMPROVE UNDERSTANDING AND SKILL DEVELOPMENT. THESE TECHNOLOGIES MAKE EDUCATION MORE ACCESSIBLE, EXPERIENTIAL, AND FUTURE-READY.



DOMAIN #6

ENERGY AND ENVIRONMENTAL STABILITY

SUSTAINABLE HARDWARE INNOVATIONS FOCUS ON EFFICIENT ENERGY GENERATION, STORAGE, AND ENVIRONMENTAL MONITORING. SOLUTIONS SUCH AS RENEWABLE ENERGY SYSTEMS, SMART GRIDS, AND POLLUTION MONITORING DEVICES PROMOTE RESPONSIBLE RESOURCE UTILIZATION. THESE TECHNOLOGIES CONTRIBUTE TO REDUCING ENVIRONMENTAL IMPACT AND SUPPORTING A GREENER FUTURE.





DOMAINS



DOMAIN #7

INDUSTRIAL AUTOMATION AND CONTROL SYSTEMS

INDUSTRIAL AUTOMATION UTILIZES HARDWARE-BASED CONTROL SYSTEMS TO IMPROVE PRODUCTIVITY, PRECISION, AND SAFETY IN MANUFACTURING PROCESSES. TECHNOLOGIES SUCH AS SENSORS, CONTROLLERS, AND MONITORING SYSTEMS ENABLE EFFICIENT OPERATION AND REAL-TIME DECISION-MAKING. AUTOMATION PLAYS A CRUCIAL ROLE IN MODERN INDUSTRIAL TRANSFORMATION.

DOMAIN #8

STUDENT INNOVATION

THIS DOMAIN ENCOURAGES STUDENTS TO EXPLORE CREATIVE AND UNCONVENTIONAL HARDWARE SOLUTIONS BEYOND PREDEFINED CATEGORIES. PARTICIPANTS CAN PRESENT ORIGINAL IDEAS ADDRESSING REAL-WORLD PROBLEMS THROUGH INNOVATION AND EXPERIMENTATION. STUDENT INNOVATION PROMOTES CREATIVITY, INTERDISCIPLINARY THINKING, AND PRACTICAL IMPLEMENTATION.





INSTRUCTIONS

- THE HACKATHON WILL SPAN 48 HOURS, DURING WHICH TEAMS MUST PROPOSE A SOLUTION WITHIN ONE OF THE SEVEN PROVIDED DOMAINS. TEAMS MAY CHOOSE ANY PROBLEM STATEMENT WITHIN THEIR SELECTED DOMAIN; HOWEVER, ONCE THE ROUND 1 PRESENTATION IS COMPLETED, THE CHOSEN DOMAIN AND PROBLEM STATEMENT WILL BE CONSIDERED FINAL AND CANNOT BE CHANGED.
- TEAMS MAY USE ANY COMPONENTS TO DEVELOP THEIR SOLUTION. THIS INCLUDES (BUT IS NOT LIMITED TO):

1. CONTROLLERS: ARDUINO, RASPBERRY PI, STM32, MSP430, FPGA, 8051, ETC.
2. SENSORS: PROXIMITY, HUMIDITY, IR, HAPTIC, AUDIO, PHOTOSENSORS, ETC.

QUALIFIED TEAMS MUST SUBMIT THEIR REQUIRED COMPONENT LIST AFTER ROUND 1 VIA A GOOGLE FORM. COMPONENTS AVAILABLE IN THE COLLEGE INVENTORY WILL BE PROVIDED, WHILE ANY ADDITIONAL COMPONENTS MUST BE ARRANGED BY THE TEAMS THEMSELVES.

ANY UNAVAILABILITY WILL BE COMMUNICATED TO THE TEAMS WELL IN ADVANCE.

- INTERNET FACILITIES WILL BE PROVIDED AT THE VENUE. PARTICIPANTS ARE FREE TO REFER TO ANY ONLINE PLATFORMS, DOCUMENTATION, OR RESOURCES TO AID IN THE DEVELOPMENT OF THEIR SOLUTION.
- ELIGIBILITY: PARTICIPANTS CAN BE STUDENTS OF ANY YEAR PURSUING B.E. / B.TECH FROM ANY DEGREE COLLEGE OR STUDENTS FROM ANY TECHNICAL DIPLOMA COLLEGE. (OPEN TO ALL BRANCHES)
- THE EVENT SHALL TAKE PLACE IN TWO ROUNDS:

ROUND 1 – PRELIMINARY PRESENTATION (ONLINE)

TEAMS WILL PRESENT THEIR SELECTED DOMAIN, PROBLEM STATEMENT, PROPOSED SOLUTION APPROACH, AND TENTATIVE COMPONENTS.

SHORTLISTED TEAMS WILL ADVANCE TO THE NEXT ROUND.

REGISTRATION FEE: RS.200 PER TEAM (NON-REFUNDABLE)

ROUND 2 – HACKATHON & FINAL DEMONSTRATION

QUALIFIED TEAMS WILL PARTICIPATE IN A 48-HOUR ON-CAMPUS HACKATHON TO DEVELOP AND IMPLEMENT THEIR SOLUTION, FOLLOWED BY A FINAL PROTOTYPE DEMONSTRATION AND EVALUATION BY THE JUDGING PANEL.

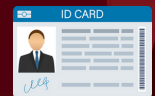
REGISTRATION FEE: RS.600 PER TEAM FOR 2-3 MEMBERS

RS.800 PER TEAM FOR 4 MEMBERS (RS.600 + RS.200 ADDITIONAL MEMBER CHARGE)



STUDENTS CODE OF CONDUCT

- EACH PARTICIPANT MUST HAVE THEIR COLLEGE IDENTITY (ID) CARD WITH THEM WHILE ON CAMPUS. THEY ARE REQUIRED TO CARRY THEIR REGISTRATION BAND AT ALL TIMES.
- PARTICIPANTS ARE EXPECTED TO FOLLOW THE TIMING AS SPECIFIED IN THE SCHEDULE AND ARRIVE AT THE VENUE ON TIME TO AVOID ANY HASSLE.
- IT IS MANDATORY FOR ALL THE PARTICIPANTS TO STAY ON CAMPUS FOR THE ENTIRE DURATION OF THE EVENT.
- PARTICIPANTS ARE EXPECTED TO READ AND FOLLOW NOTICES AND UPDATES POSTED ON OUR WEBSITE AND INSTAGRAM FROM TIME TO TIME.
- BE POLITE AND RESPECTFUL TOWARDS STAFF (TEACHING, NON-TEACHING, AND ADMINISTRATIVE), ORGANIZERS, AND FELLOW PARTICIPANTS.
- PARTICIPANTS MUST HELP TO KEEP THE CAMPUS NEAT AND CLEAN. DO NOT SCRIBBLE ON THE DESKS, THE BULLETIN BOARDS, OR THE WALLS OR TREES OF THE COLLEGE. SPITTING, SMOKING, AND THROWING BITS OF PAPER ON THE PREMISES ARE PROHIBITED.
- MISUSE OR UNAUTHORIZED USE OF THE INSTITUTE PREMISES OR ITEMS WILL LEAD TO DISCIPLINARY AND PENAL ACTION.
- PARTICIPANTS SHALL DO NOTHING, EITHER INSIDE OR OUTSIDE THE INSTITUTE, THAT WILL IN ANY WAY INTERFERE WITH ITS ORDERLY CONDUCT AND DISCIPLINE.





REGISTRATION PROCESS

THINK & TAKE OVER ! BUT FIRST,
REMEMBER TO REGISTER
ELECTROVERSE.COMM-TSEC,
INVITES ALL THE TECH-GEEKS TO
REGISTER FOR TECH-A-THON
(THE HARDWARE HACKATHON) !

REGISTRATION FEES:

RS. 200 PER TEAM
(NON-REFUNDABLE)
FOR 1ST ROUND



REGISTER NOW



[REGISTRATION LINK](#)

FOR MORE EVENT RELATED INFORMATION,
CHECK OUT ON INSTAGRAM
[@electroverse.comm_tsec](#)



FAQ'S

- 1. WHO CAN PARTICIPATE IN THIS HACKATHON?** - PARTICIPANTS CAN BE A STUDENT OF ANY YEAR OF THE B.E.(BACHELOR OF ENGINEERING)/ B.TECH DEGREE COLLEGE OR ANY TECHNICAL DIPLOMA COLLEGE.
 - 2. WRONG DETAILS ARE SUBMITTED IN THE REGISTRATION FORM, HOW CAN THEY BE CORRECTED?** - CONTACT THE STUDENT COORDINATORS (CONTACT DETAILS ARE MENTIONED ON THE NEXT PAGE) OR MAIL TO ELECTROVERSECOMMTSEC@GMAIL.COM
 - 3. WILL FOOD AND WIRELESS INTERNET ACCESS BE PROVIDED?** - YES, THERE WILL BE. HOWEVER, IT IS ALWAYS BETTER TO BE PREPARED: IF YOU HAVE A BACKUP OPTION, PLEASE FEEL FREE TO BRING IT ALONG AND FOOD, LUNCH, SNACKS, AND DINNER WILL BE PROVIDED THROUGHOUT THE EVENT.
 - 4. WHAT SHOULD TEAMS CARRY TO THE HACKATHON VENUE?** - STUDENTS ARE REQUESTED TO BRING THEIR OWN DEVICES AND OTHER NECESSARY EQUIPMENT REQUIRED FOR THE EVENT. THE COMPONENTS REQUIRED TO BUILD THE SOLUTION ARE TO BE INFORMED BY THE QUALIFIED TEAMS AFTER ROUND 1, WHICH WILL BE ARRANGED FOR THEM BY THE THADOMAL SHAHANI ENGINEERING COLLEGE.
 - 5. WHAT IS THE BENEFIT OF PARTICIPATING?** - SELF-ASSESS YOUR ABILITIES WHILE COORDINATING WITH YOUR TEAM, GET TIPS FROM THE EXPERTS, ENHANCE SOCIAL SKILLS, SEIZE THE OPPORTUNITY TO WIN PRIZE MONEY OF UP TO RS 25,000/- AND HAVE FUN ALONG WITH AN EFFECTIVE AND MEMORABLE LEARNING EXPERIENCE.
 - 6. IS IT MANDATORY TO FORM TEAMS WITH ALL MEMBERS FROM THE SAME COLLEGE?** - NO, YOU CAN FORM YOUR TEAM WITH ALL MEMBERS WHO QUALIFY THE ELIGIBILITY CRITERIA.
- (NOTE - CIRCUIT DIAGRAM INFORMATION DIRECTLY TAKEN FROM INTERNET WITHOUT ANY INNOVATION IN IDEA WILL LEAD TO ELIMINATION.)**



THADOMAL SHAHANI
TSEC
ENGINEERING COLLEGE



CONTACT US



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tsec.electroverse@gmail.com



<https://www.electroverse-comm.in/>



unstop.com/tech-a-thon-hardware-hackathon



youtube.com/@tsecextcdepartment8926

**FOR REGISTRATION-RELATED
QUERIES STUDENT CO-ORDINATORS**

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FOR MORE EVENT RELATED INFORMATION,

CHECK OUT ON INSTAGRAM

[@electroverse.comm_tsec](https://www.instagram.com/electroverse_comm_tsec)